

FIG. 1

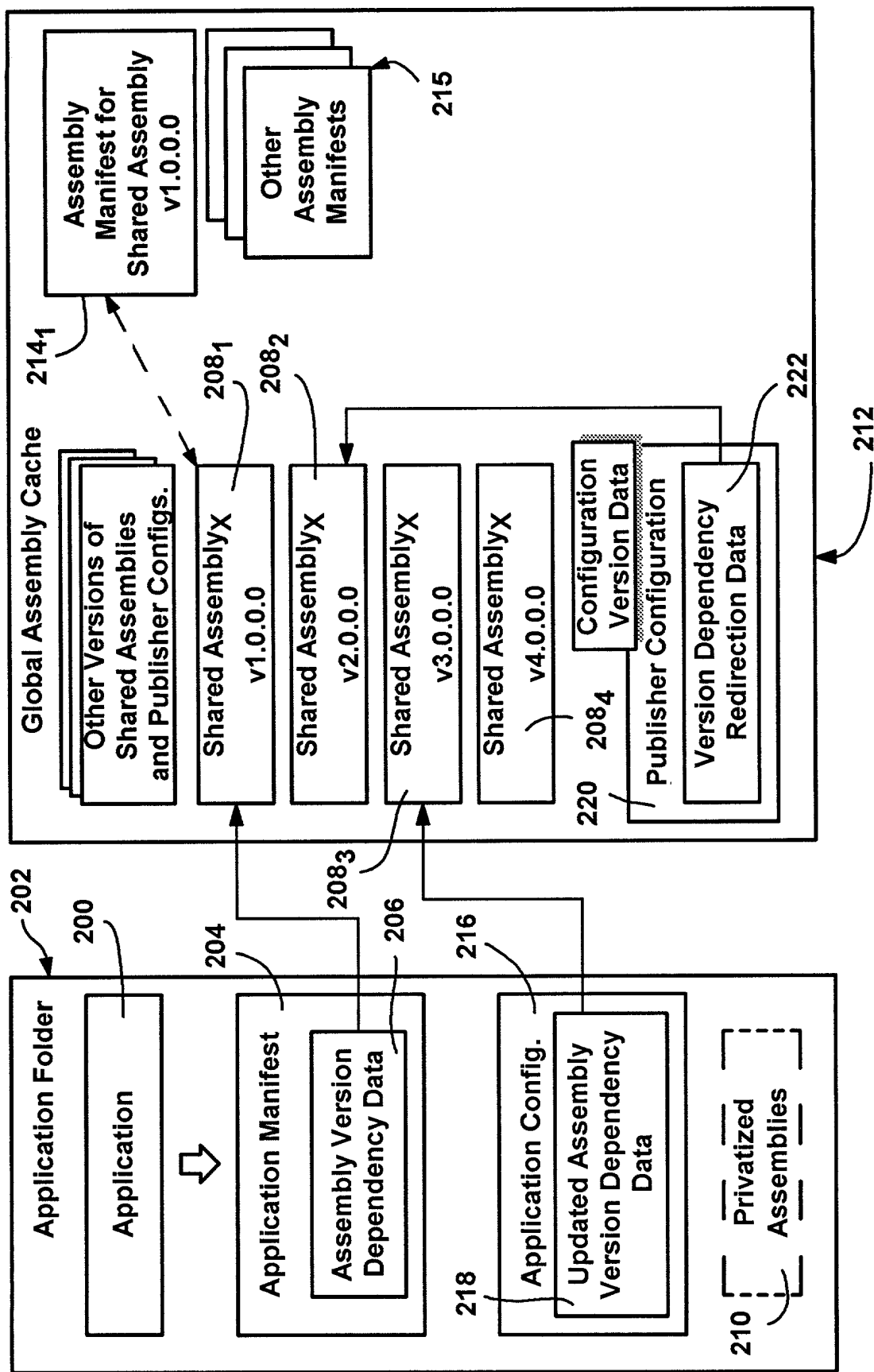


FIG. 2A

FIG. 3A is a block diagram of a system architecture for application activation. The diagram shows the flow of data and control between various components during the activation process.

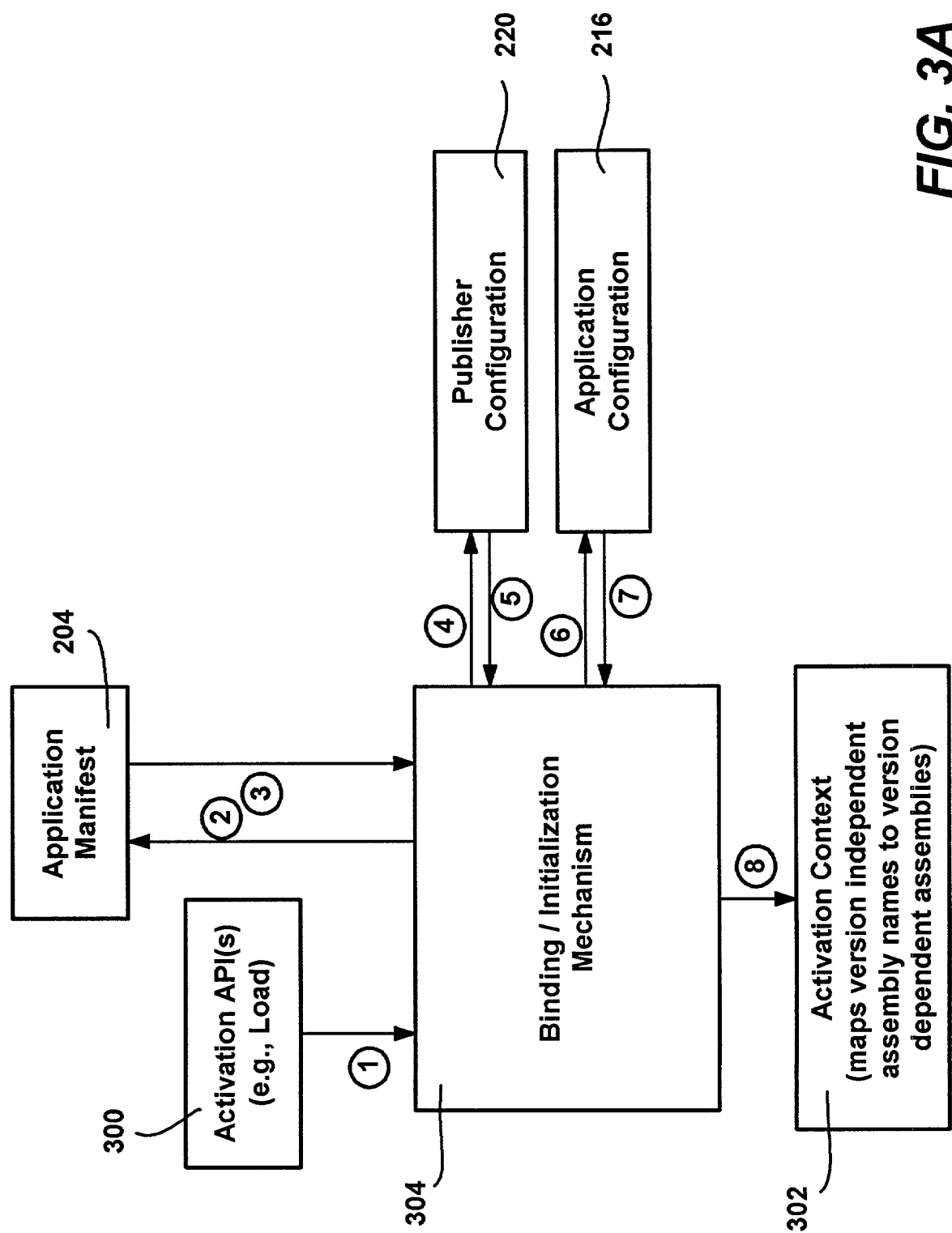


FIG. 3A

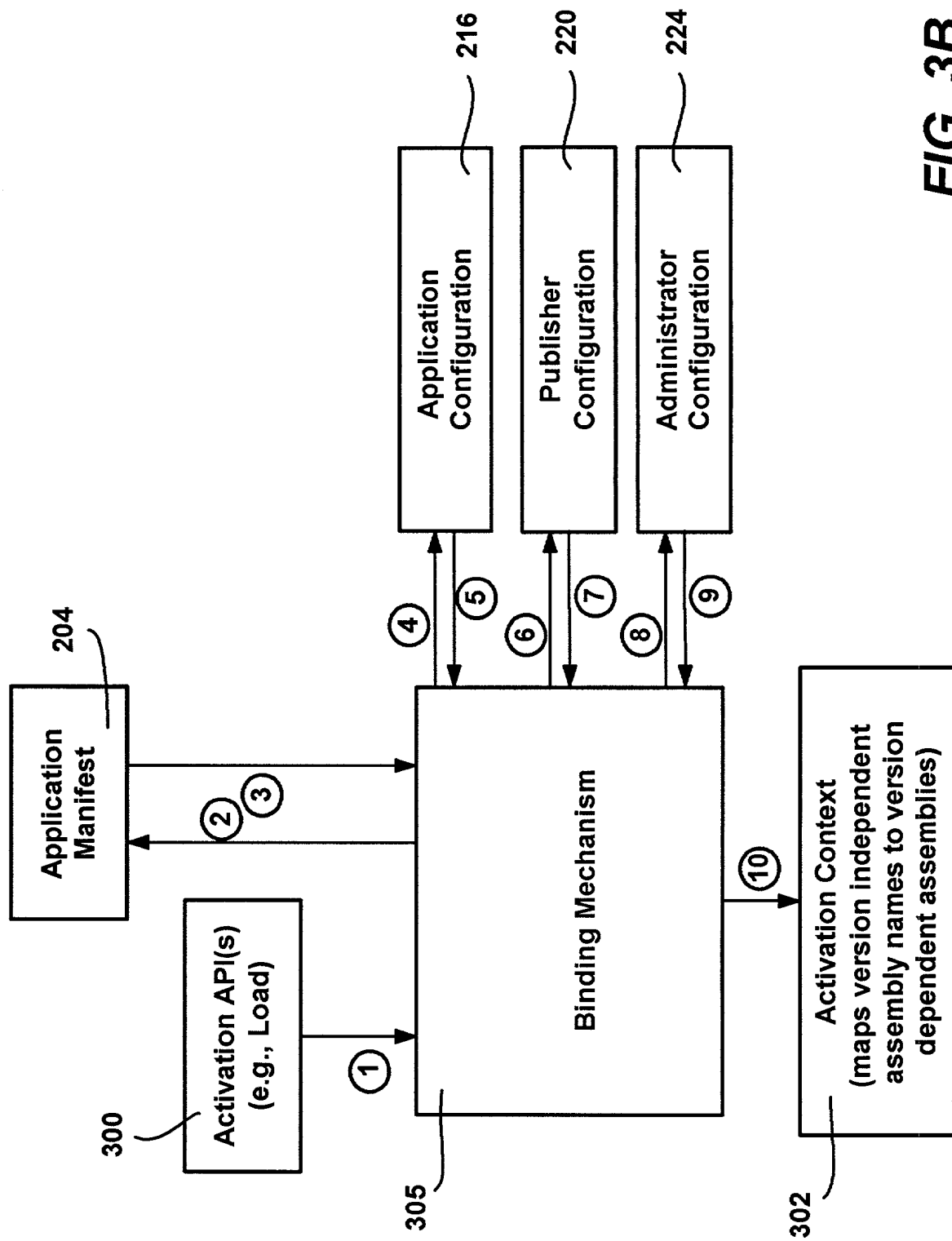


FIG. 3B

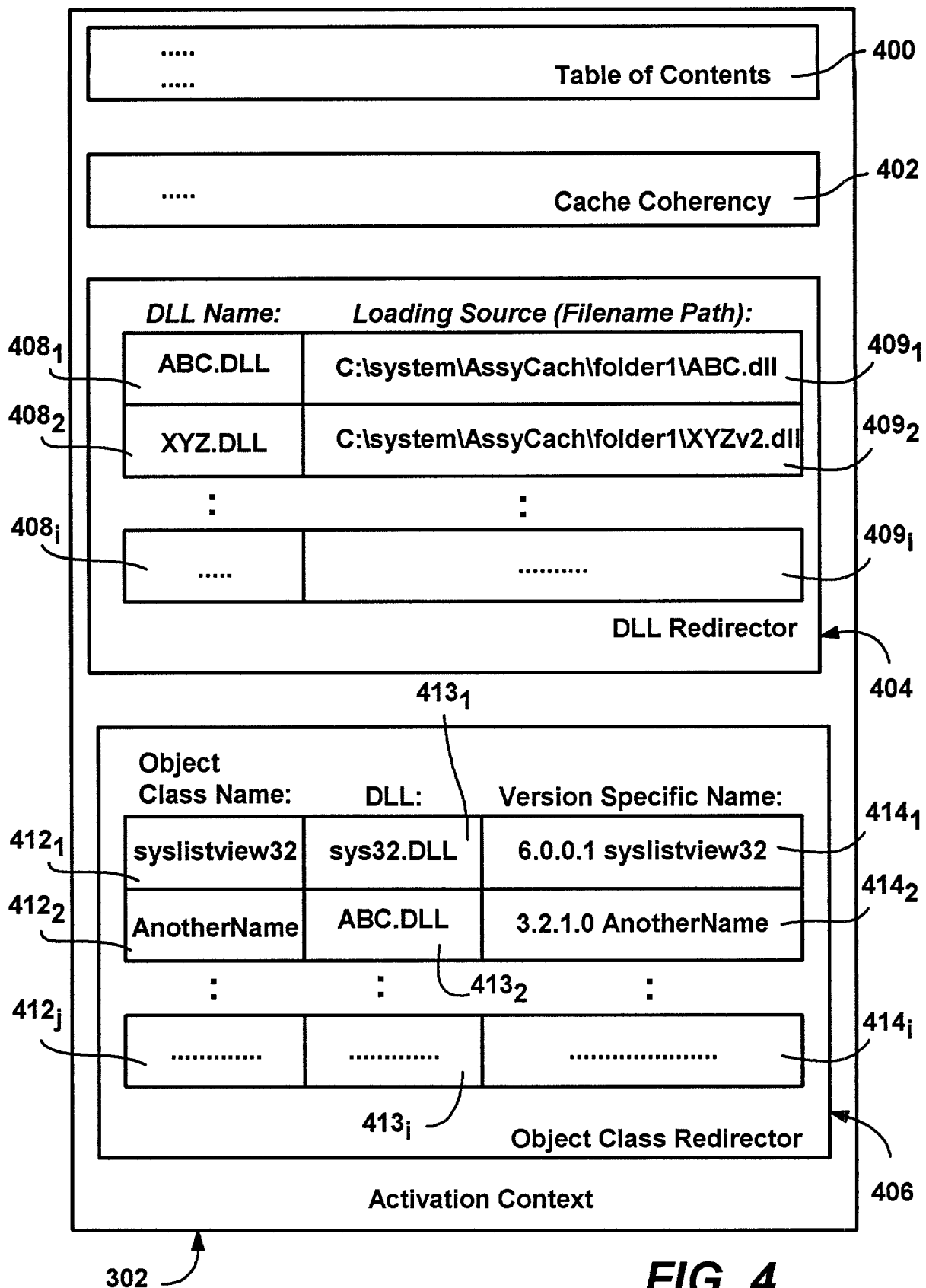


FIG. 4

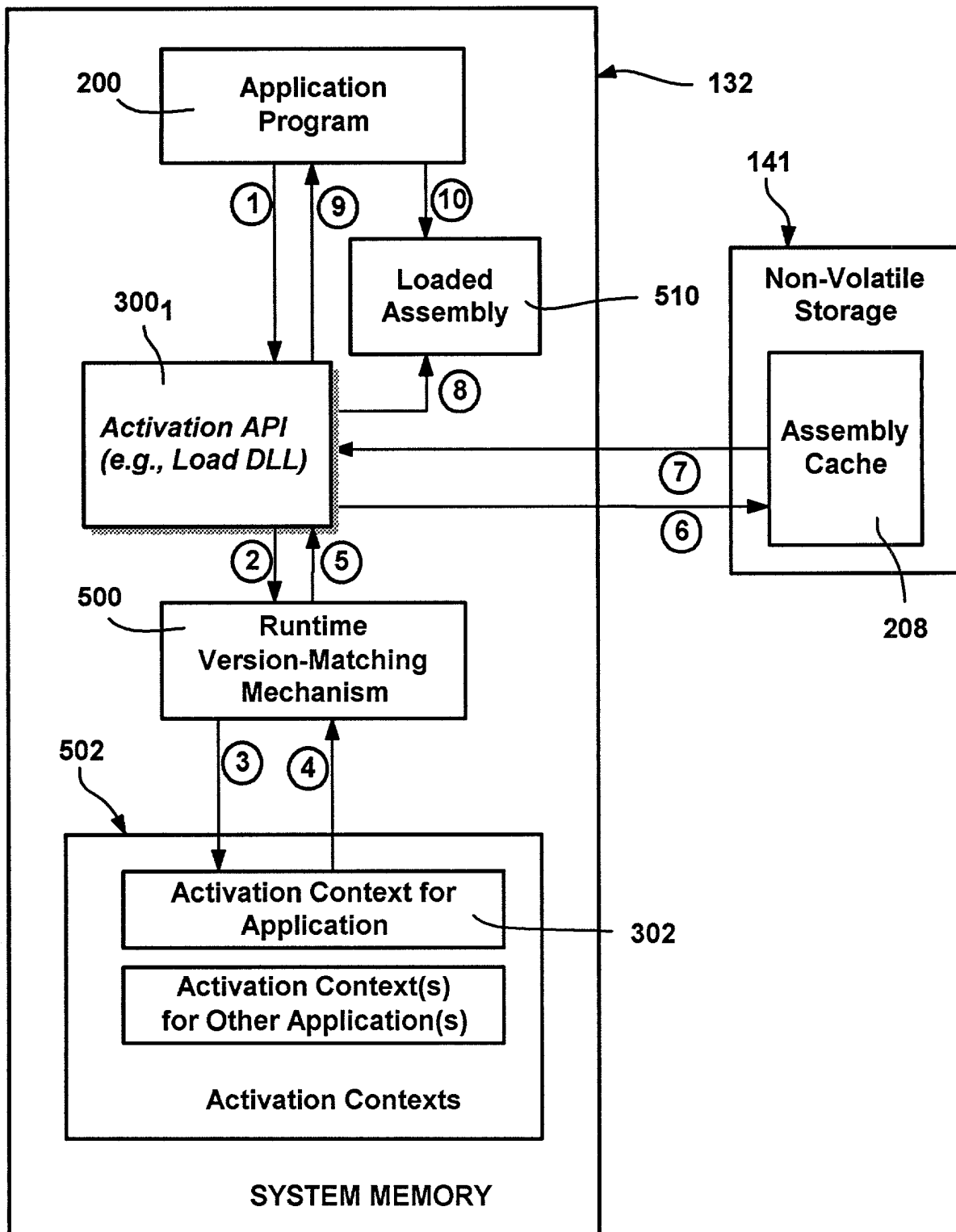


FIG. 5

FIG. 6

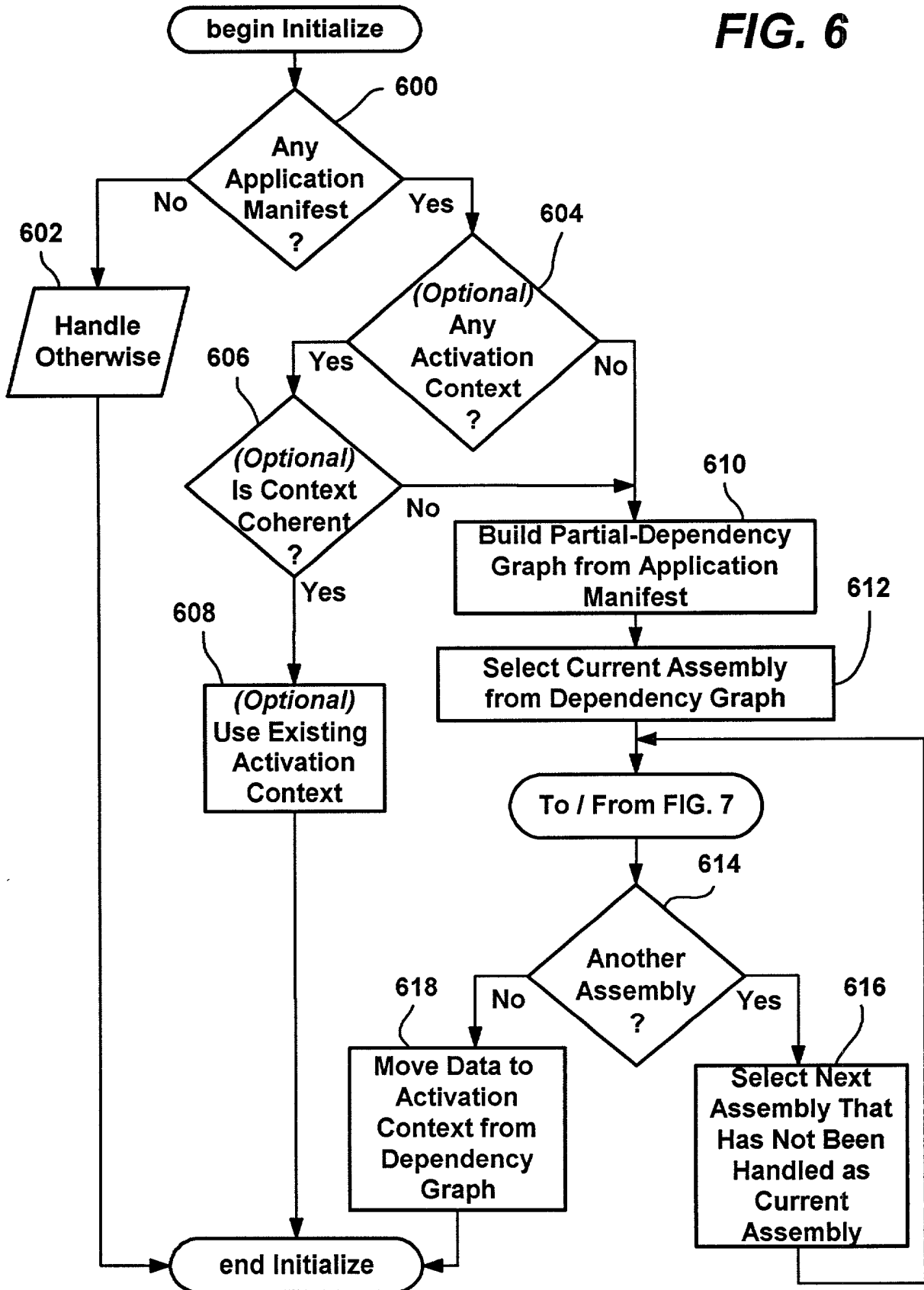
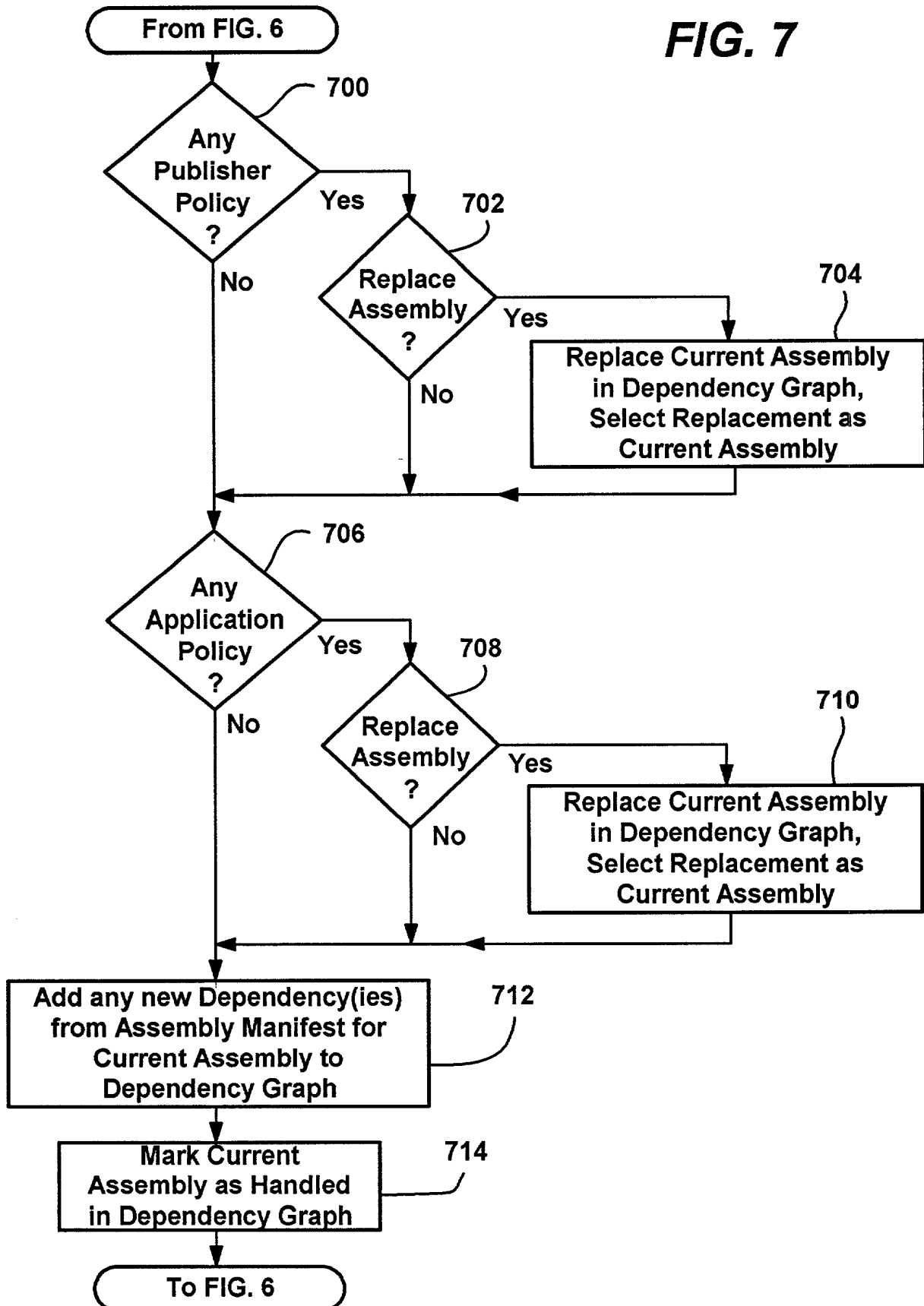


FIG. 7



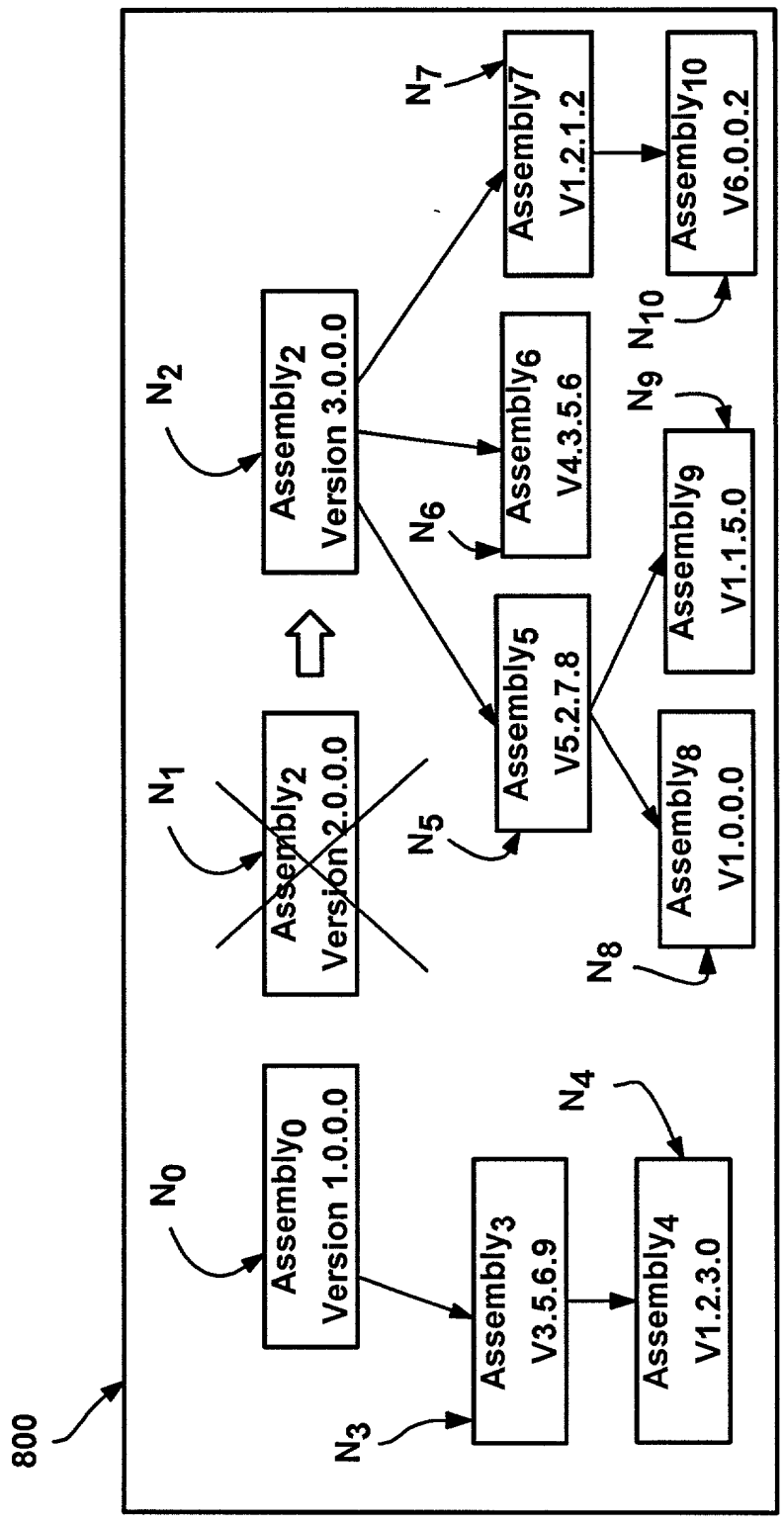


FIG. 8

FIG. 9

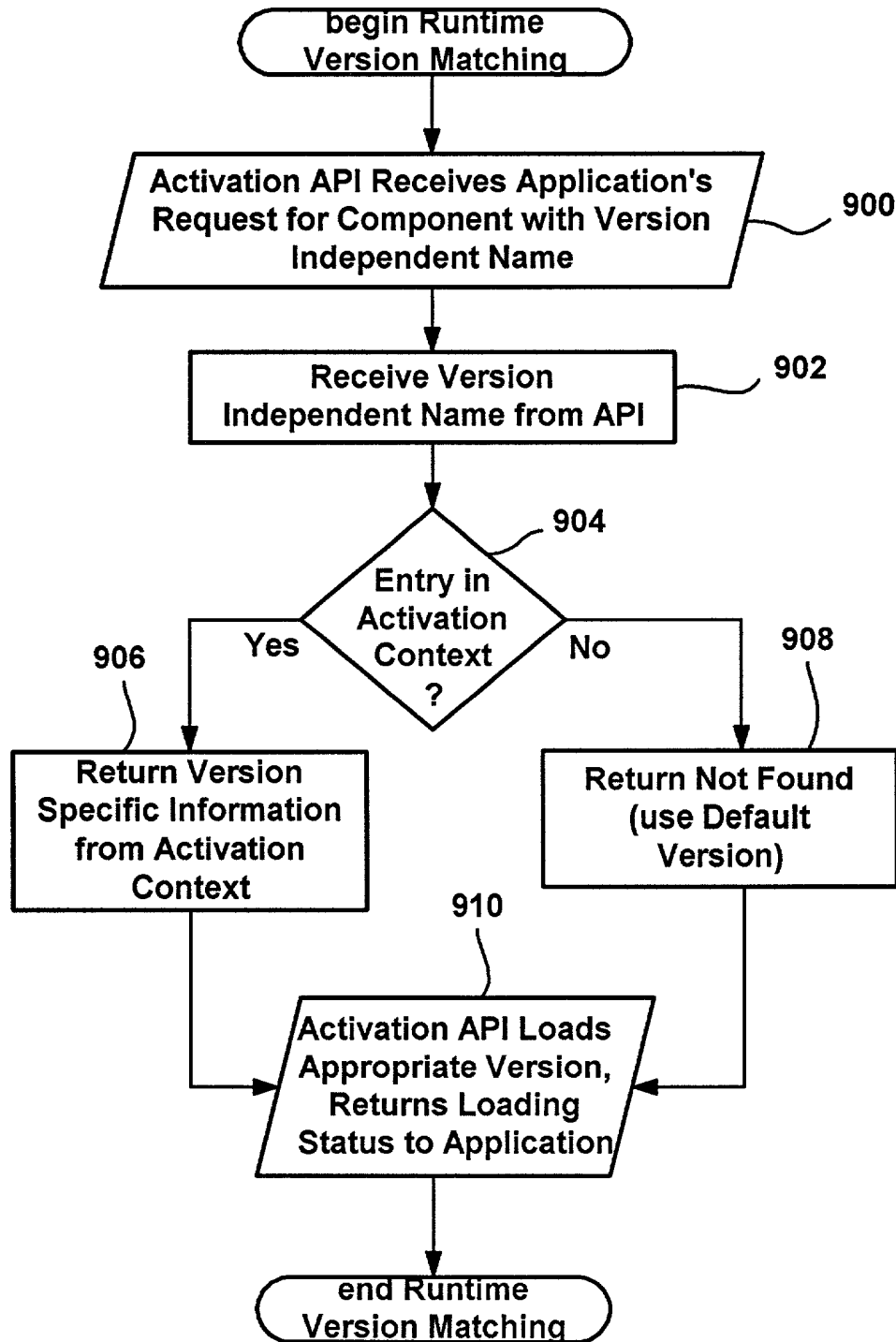


FIG. 10

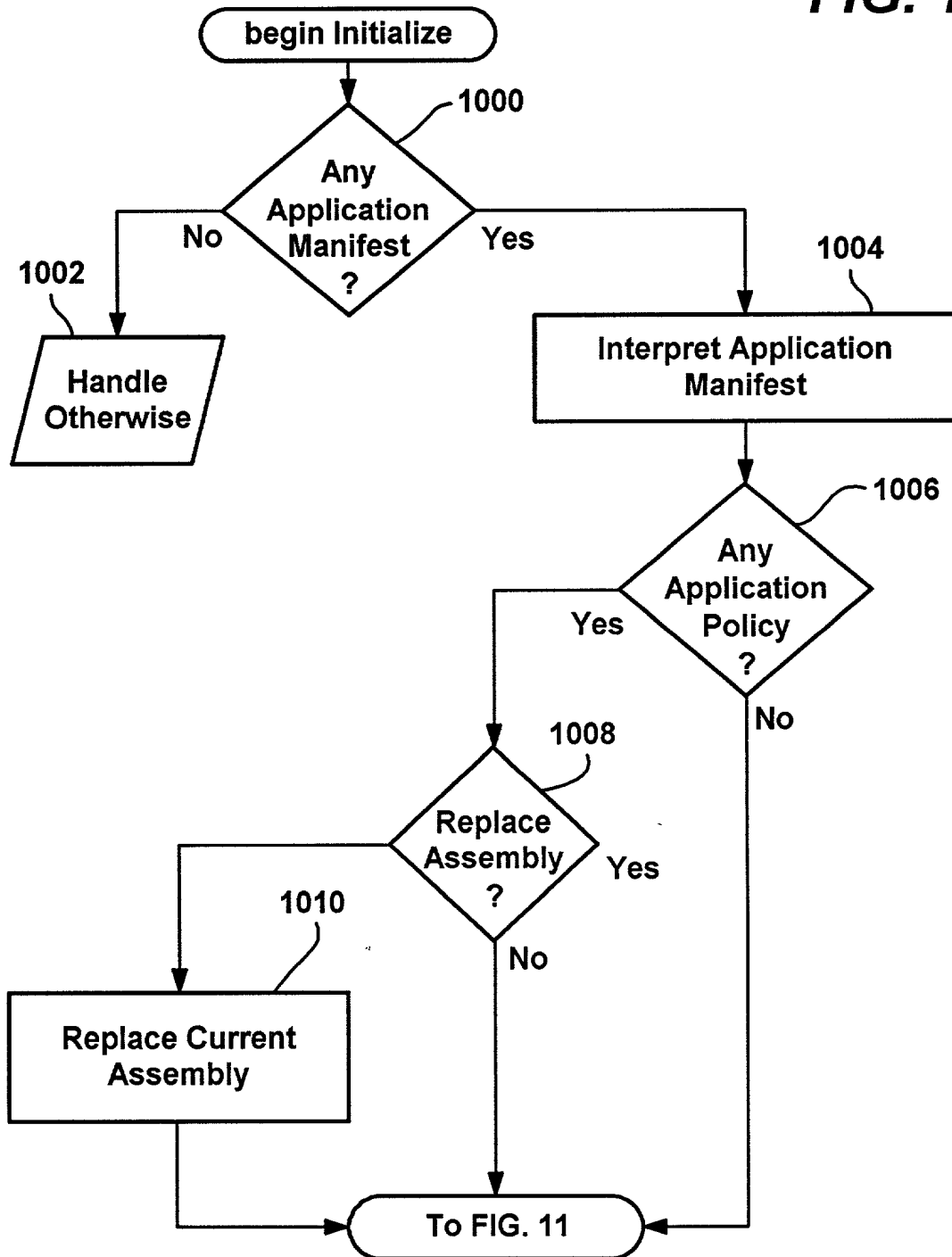


FIG. 11

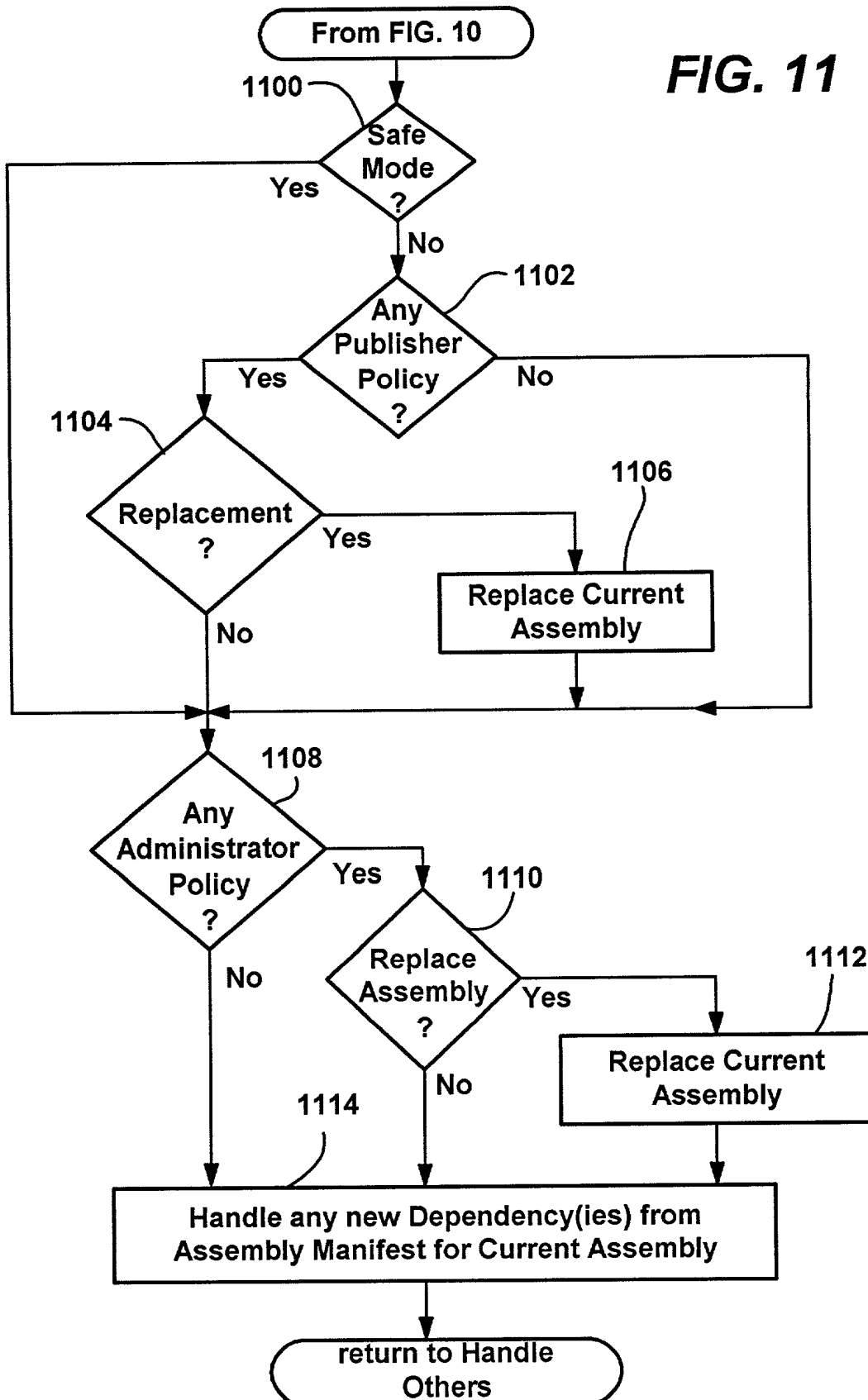


FIG. 12

